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The program counter in 8085 microprocessor is a 16-bit register, because. It counts 16 bits at a time. There are 16 address times. It facilitates the users storing 16-bit data temporarily. It has to fetch two 8-bit data at

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microprocessor, the

value of the most

significant bit of the

result following the

execution of any

arithmetic or Boolean

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instruction is stored in
the A) carry status flag

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Ans. Accumulator is an
8 bit register which
stores data and
performs arithmetic
and logical operations.
The result of the
operation is stored in

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the accumulator. It is designated by the letter 'A'. 3) What are the different types of flags in 8085 microprocessor? Interview Questions on ...

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minimum number of
transistors required to
implement a two input
AND gate is. A. 2 B. 4
C. 6 D. 8. 2. Using

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DeMorgan's Theorem we can convert any AND-OR structure into.
A. NAND-NAND B. OR-NAND C. NAND-NOR D. NOR-NAND.

3. For a memory with a 16-bit address space, the address ability is A. 16 bits B. 8 bits C. 2^{16} bits

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Which is the
microprocessor
comprises: a. 3
Accumulator based
microprocessor
example are: a.
Motorola.A

microprocessor is a
chip integrating all the
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26. The Intel 8086 microprocessor is a _____ processor. a) 8 bits. b) 4 bits. c) 16 bits. d) 32 bits. Answer: c .

27. What is /are the improvement is in the architecture of 8086 over 8085 architecture. a) A 16-bit ALU, a set of 16-bit registers. b) Segmented memory addressing. c) Fetched

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instruction queue for overlapped fetching and execution. d) All of the mentioned

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The 8085
microprocessor has
five interrupt inputs.
They are TRAP, RST
7.5, RST 6.5, RST 5.5,
and INTR. These
interrupts have a fixed
priority of interrupt

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service. If two or more interrupts go high at the same time, the 8085 will service them on priority basis. The TRAP has the highest priority followed by RST 7.5, RST 6.5, RST 5.5.

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